

## Abstract of the Disclosure

In a method for control of a converter for conversion of dc voltage into ac voltage or dc voltage and vice versa, in which an output of the converter may alternatively be connected to a positive pole, a negative pole or a center of a dc voltage side of the converter in the form of different so-called main states, there is carried out, when changing between main states via a so-called minor commutation loop, an extra sequence in the form of a delayed turn-on of semiconductor elements in one unit of the converter relative to semiconductor elements in another unit in a pair of units of the converter.